Blakely, Sokoloff, Taylor & Zafman LLP (714) 557-3800 Title: DYNAMICALLY RECONFIGURING CLOCK DOMAINS ON A CHIP

1st Named Inventor: Sunil B. Chaudhari Express Mail No.: EV323394216US

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clack Domains on an Internet Circuit

Design Logic of Circuit Components to Achieve a Clock skew Less trana Prendermined Minimum.

Design Clock Distribution
Tree to Achieve Desired
Clock Skew Across - All
Combinable Circuit Component
and Logic Bocks of the IC.

Potine Two Paths
for Signals (e.g. Schodulk
by software); 1) Perault
Path; 2) Bypass Path.

Figure 1

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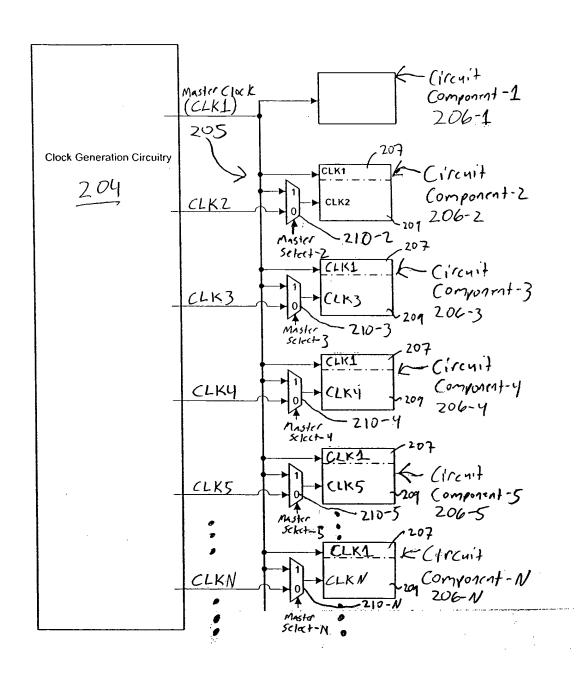
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Integrated Clicuit 200



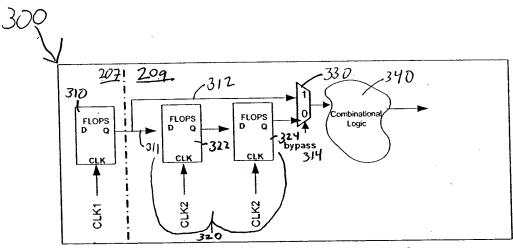
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Circuit Component-2 206-2

Figure 3

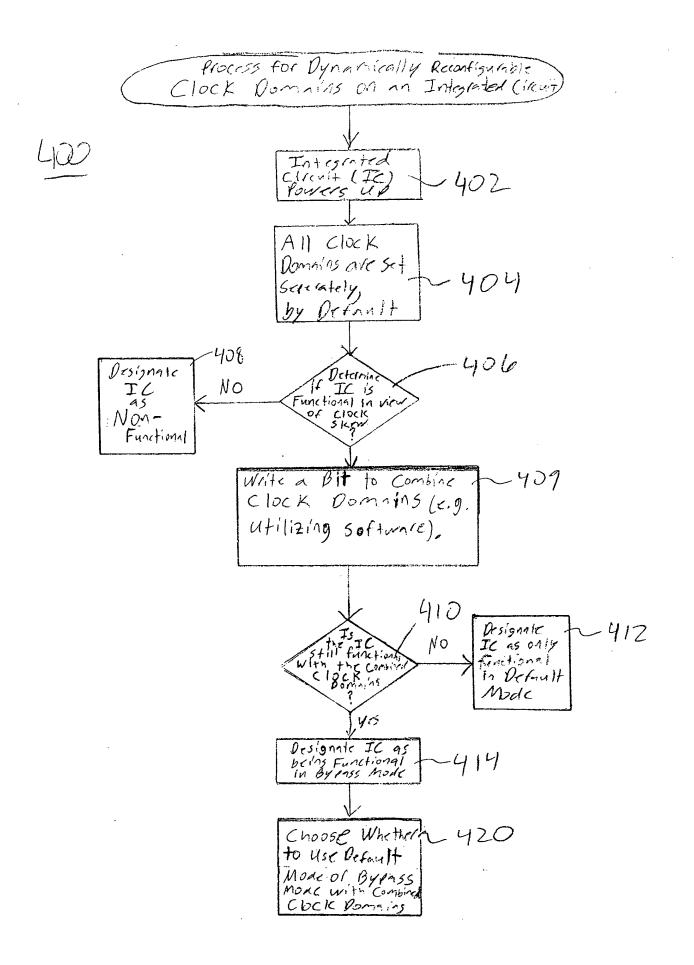


Figure 4 BEST AVAILABLE COPY

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